

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	10/742120	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:31
L2	94	(silicon adj mirror) with (method process\$3)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:32
L3	64	2 and (substrate wafer) with (front back first second)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:52
L4	8	3 and thermal near3 actuator	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:48
L5	0	"65631065"	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:50
L6	0	("65631065").PN.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 14:49
L7	6	"6556737"	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:50
L8	0	"65046431"	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:49
L9	0	"65046431"	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:50

L10	7	"6504643"	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:50
L11	5	"6563106"	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:50
L12	7	"6386716"	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:51
L13	25	"6480320"	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:51
L14	1	((silicon adj mirror) with (method process\$3) and (substrate wafer) with (front back first second) and thermal adj actuator).clm.	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:53

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L14	1	((silicon adj mirror) with (method process\$3) and (substrate wafer) with (front back first second) and thermal adj actuator).clm.	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/10/25 14:53

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Active

- ✓ L1: (1) 10/742120
- ✓ L2: (94) (silicon adj mirror) with (method process\$3)
- ✓ L3: (64) 2 and (substrate wafer) with (front back first second)
- ✓ L4: (8) 3 and thermal near3 actuator
- ✓ L7: (6) "6556737"
- ✓ L10: (7) "6504643"
- ✓ L11: (5) "6563106"
- ✓ L12: (7) "6386716"
- ✓ L13: (25) "6480320"

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DBs: US_PGPUB; USPAT; EPO; JPO
Default operator: OR
Bands
Highlight of hit terms initially

"6480320"

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